IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re Application of: | |) \ Evaminou |) Examiner: Not yet assigned | |
|--|---------------------|------------------|-------------------------------|--|
| | Keng L. Wong et al. |) | , , | |
| Serial No.: | Not yet assigned |) Art Unit: | Not yet assigned | |
| Filed: | Herewith |) | | |
| For: VOLTAGE ID BASED FREQUENCY CONTROL FOR CLOCK GENERATING CIRCUIT | |)))) | | |
| Which is a Continuation of Application of: | | | | |
| Serial No: | 10/136,321 |) | | |
| Filed: | May 2, 2002 | | | |
| 0 | and for Datasta | | | |

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

FORMAL DRAWINGS SUBMISSION

Enclosed herewith for submission in the United States Patent and Trademark

Office are thirteen (13) sheets of formal drawings for the patent application
referenced above.

If there are any further charges please charge them to Deposit Account No. 02-2666. A duplicate of this sheet is enclosed for that purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 0/0, 2003 necessary Michael A. Bernadicou Reg. No. 35,934

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